



Chief Coordinator
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Student Training
Program on

Analog Electronics

8-9 Feb, 2020

by Dr Nikhil Chander & Dr Avishek
Adhikary

EECS Department, IIT Bhilai

Program Schedule

Venue of the Workshop:
EE Lab, IIT Bhilai, GEC Campus
Sejbahar, Old Dhamtari Road
Raipur 492015 Chhattisgarh

Day 1 (8 Feb 2020)

9:00 am – 10:00 am: Registration

10:00 am – 11:30 am: **Semiconductor Devices – I**

by Dr Nikhil Chander

11:30 am – 11:45 am: Tea break

11:45 am – 01:15 pm: **Semiconductor Devices – II**

by Dr Nikhil Chander

1:15 pm – 2:30 pm: Lunch Break

2:30 pm – 5:30 pm: **Op Amps and its Application**

by Dr Avishek Adhikary

5:30 pm: Evening Snacks and End of the Day's Session

Day 2 (9 Feb 2020)

10:00 am – 10:30 am: Breakfast

10:30 am – 01:30 pm: **LT Spice simulation Lab**

01:30 am – 02:30 pm: Lunch Break

2:30 pm – 4:30 pm: **Analog Filter Design**

by Dr Avishek Adhikary

4:30 pm: Valedictory Session and End of the Program

Welcome You All